

FIG. 1

ALL CELL BUSSES ARE 32 BITS WIDE AND  
RUN AT 80MHz (32 BITS \* 80MHz = 2.5Gbps).

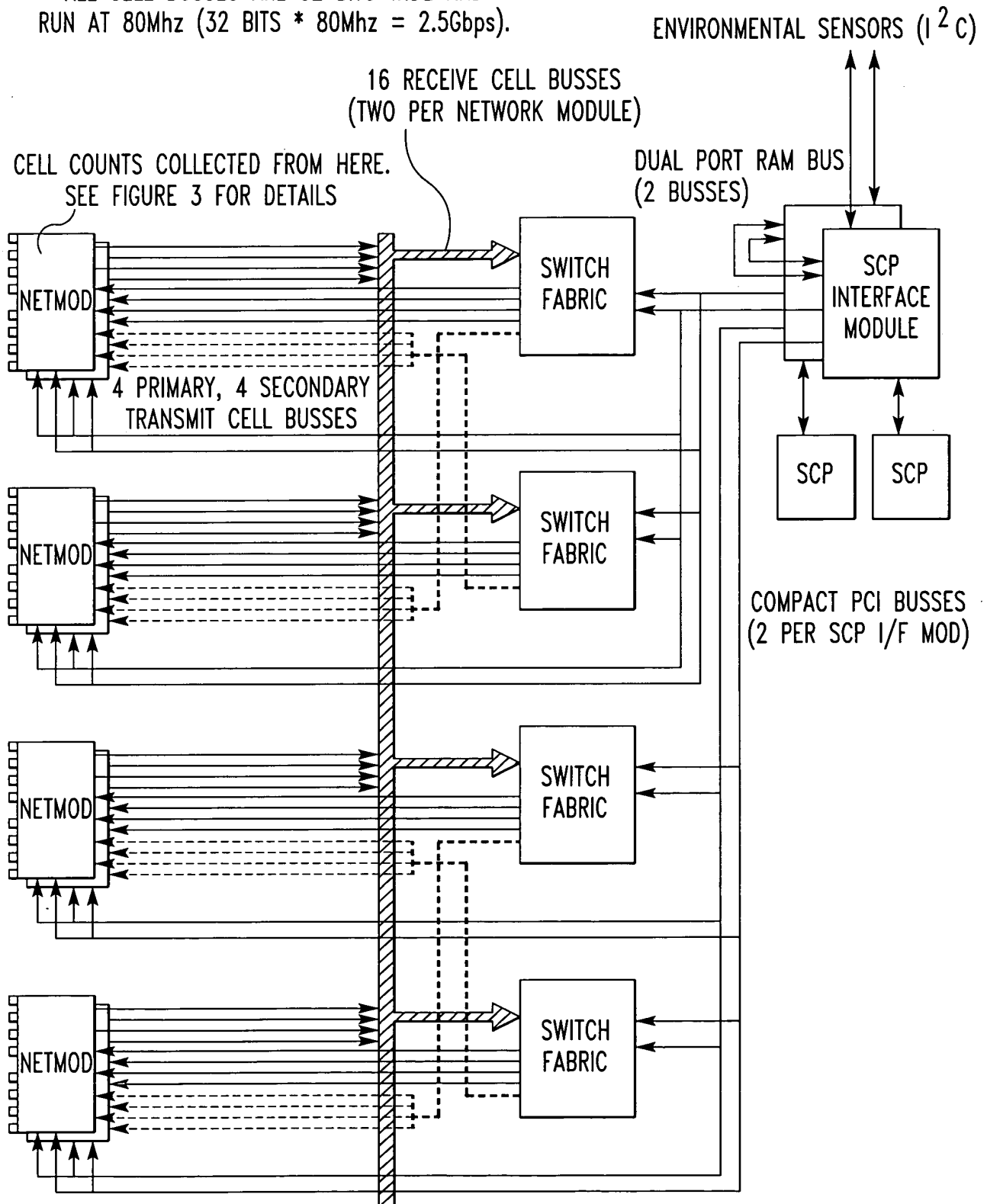


FIG.2

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QUEUEING BLOCK 1

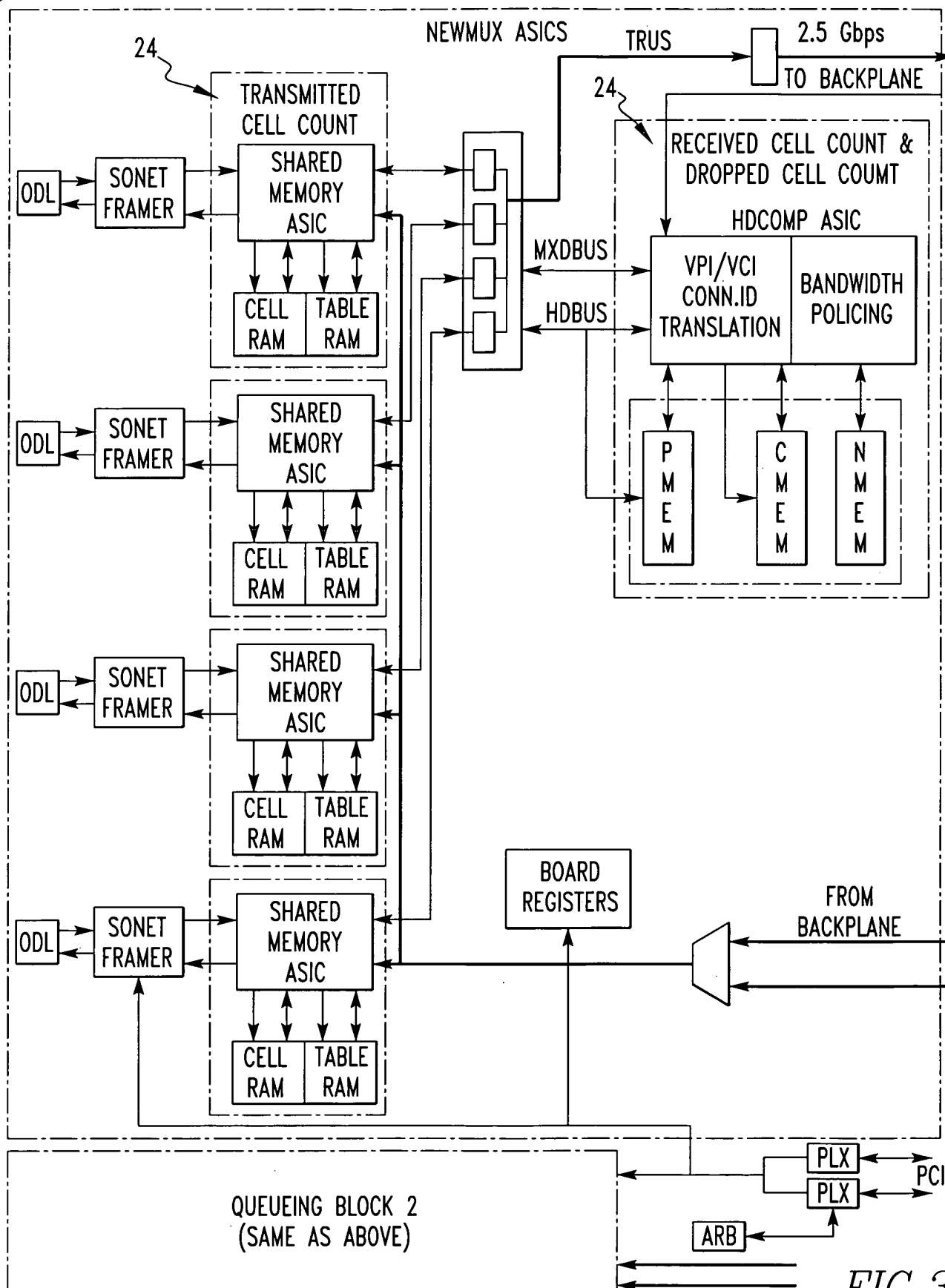


FIG.3

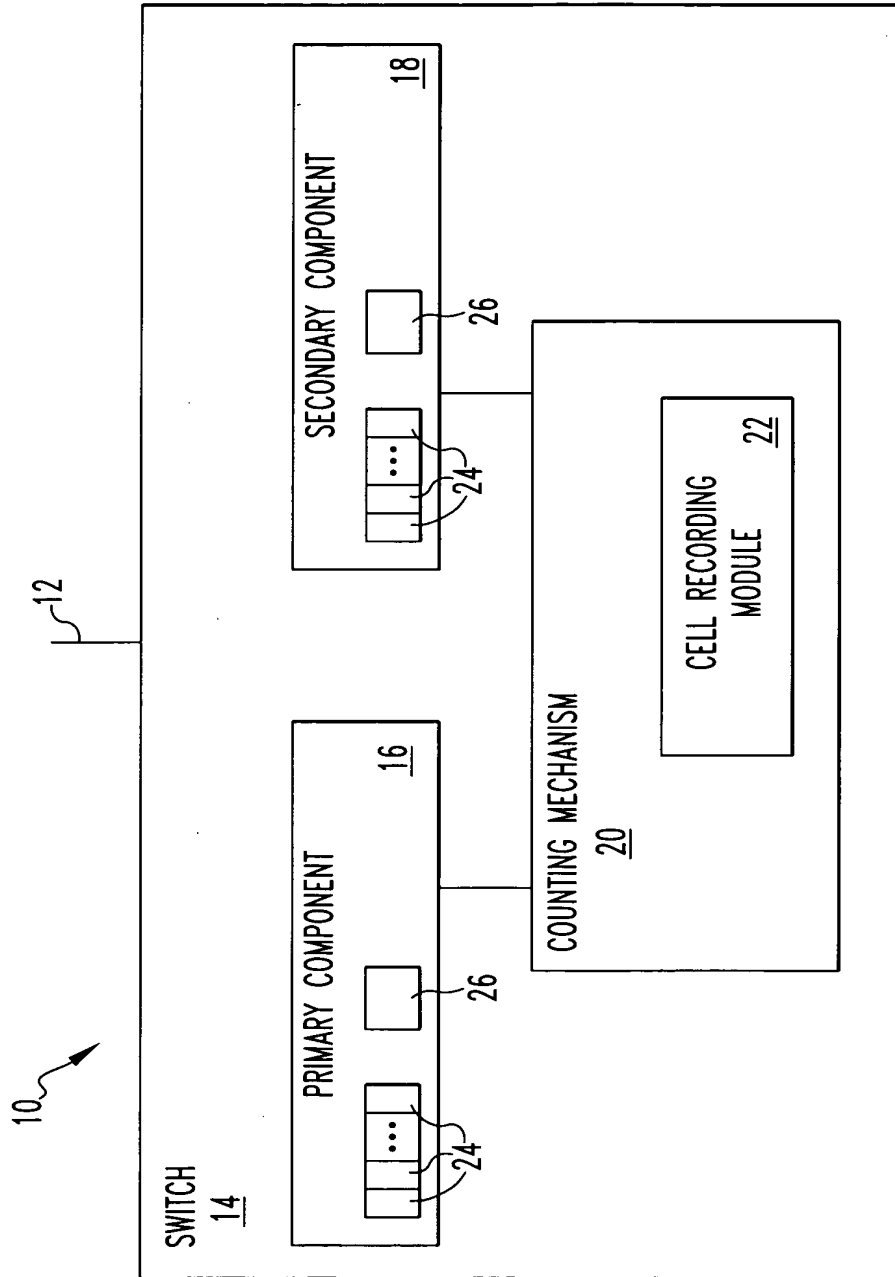


FIG. 4